

FIG. 1

10

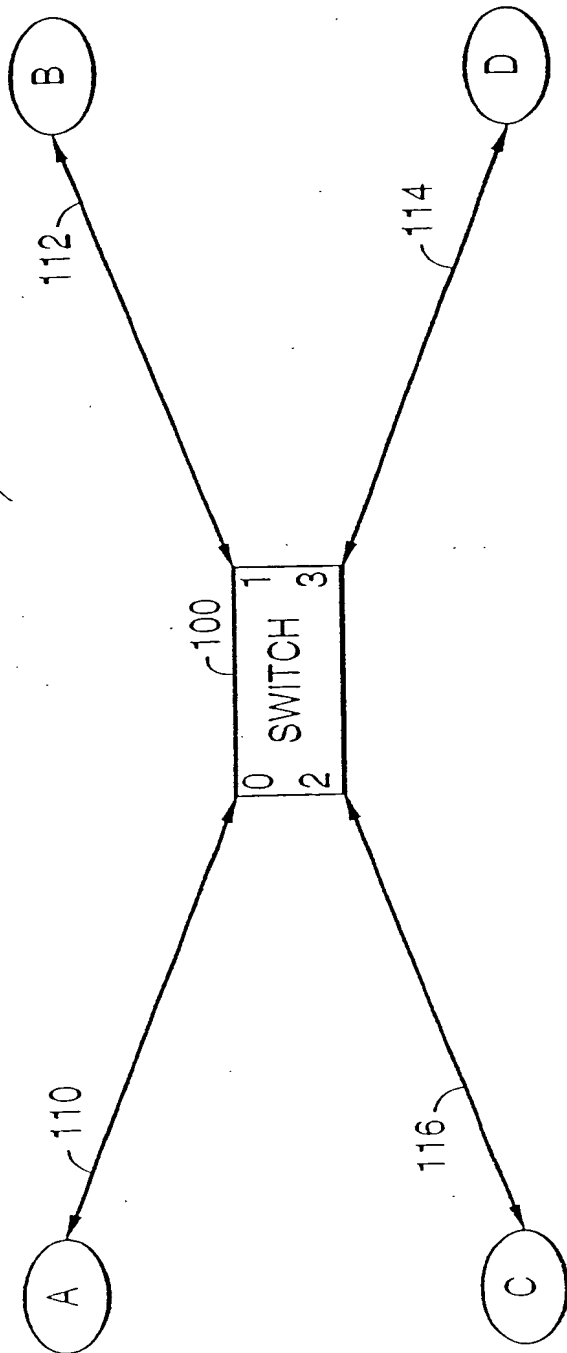


FIG. 3A

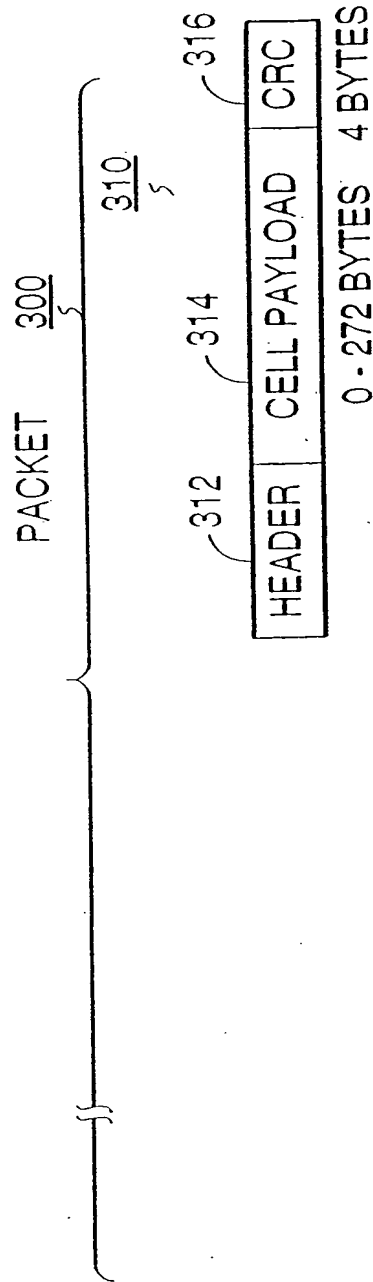
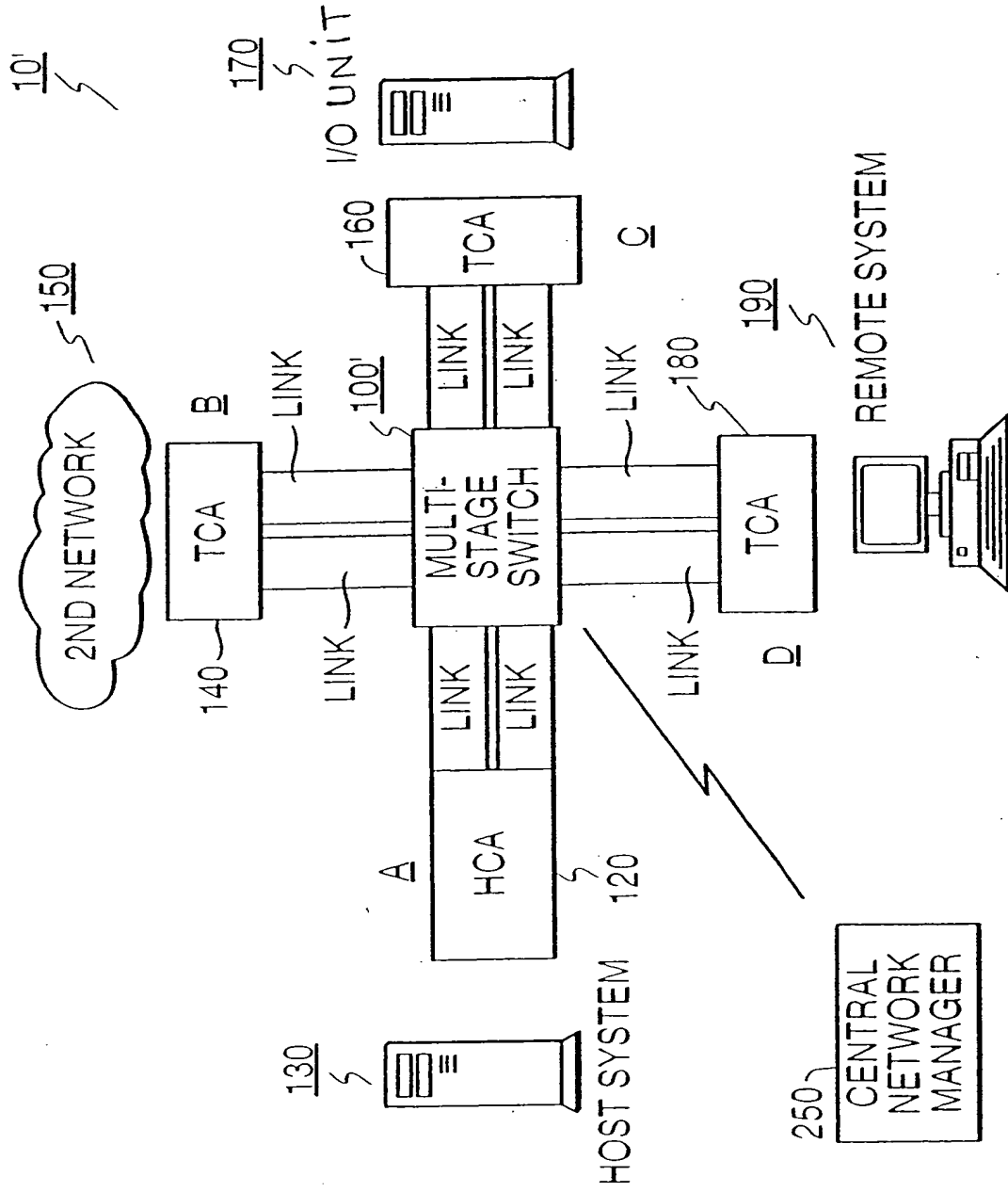


FIG. 2



9

360
5

CONTROL SEGMENT		
LENGTH	MEMORY HANDLE	VIRTUAL ADDRESS
372	374	376

370

350B

9

360

CONTROL SEGMENT		
	REMOTE MEMORY HANDLE 382	REMOTE VIRTUAL ADDRESS 384
LENGTH 372	LOCAL MEMORY HANDLE 374	LOCAL VIRTUAL ADDRESS 376

380

370

FIG. 3C

[illegible]

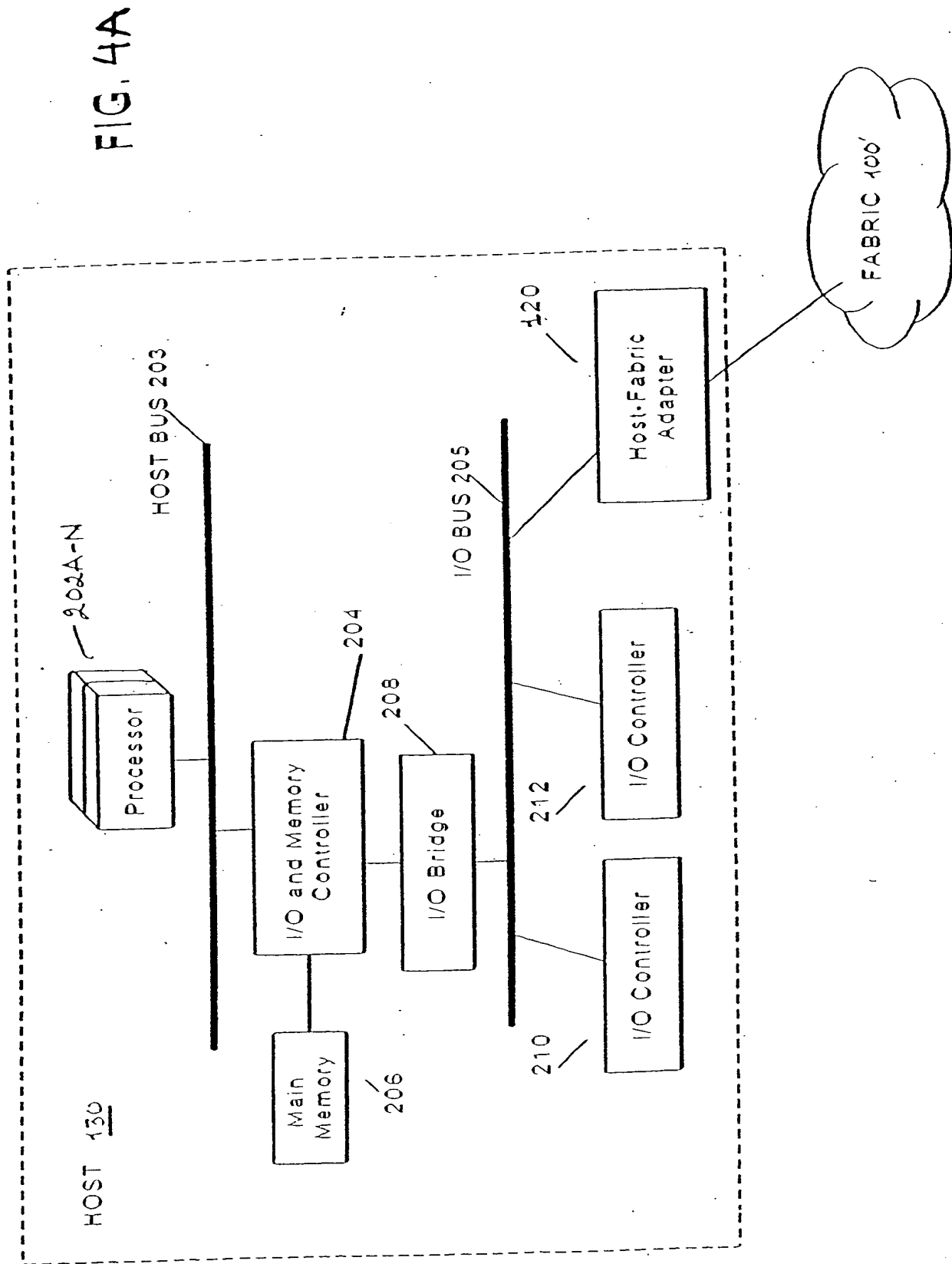


FIG. 4B

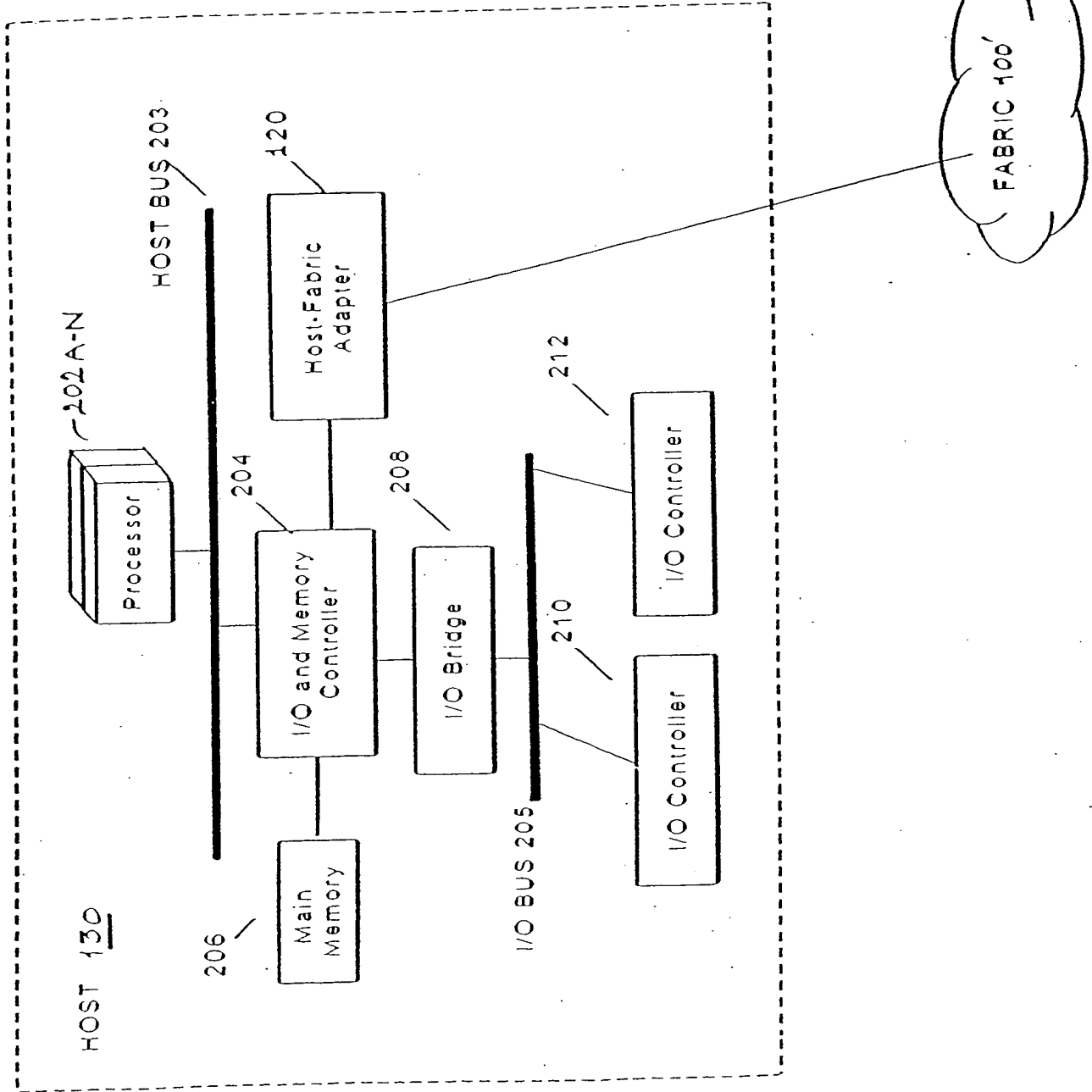


Figure 5 illustrates the architecture of a Host Operating System (OS) 500. The system is enclosed in a dashed box labeled 500. At the top is the **KERNEL** (510). Below it is the **I/O MANAGER** (520). Under the I/O Manager are two Channel Drivers: **Channel Driver A** (530A) and **Channel Driver N** (530N). Each Channel Driver contains a **Services** section. Below the Channel Drivers is a large block labeled **FABRIC BUS DRIVER PROVIDING BUS ABSTRACTION** (540). This block is connected to two **Fabric Adapter Device Specific Drivers** (550A and 550N) via bidirectional arrows. These drivers are then connected to **Host-Fabric Adapters** (120A and 120N) via bidirectional arrows.

FIG. 5

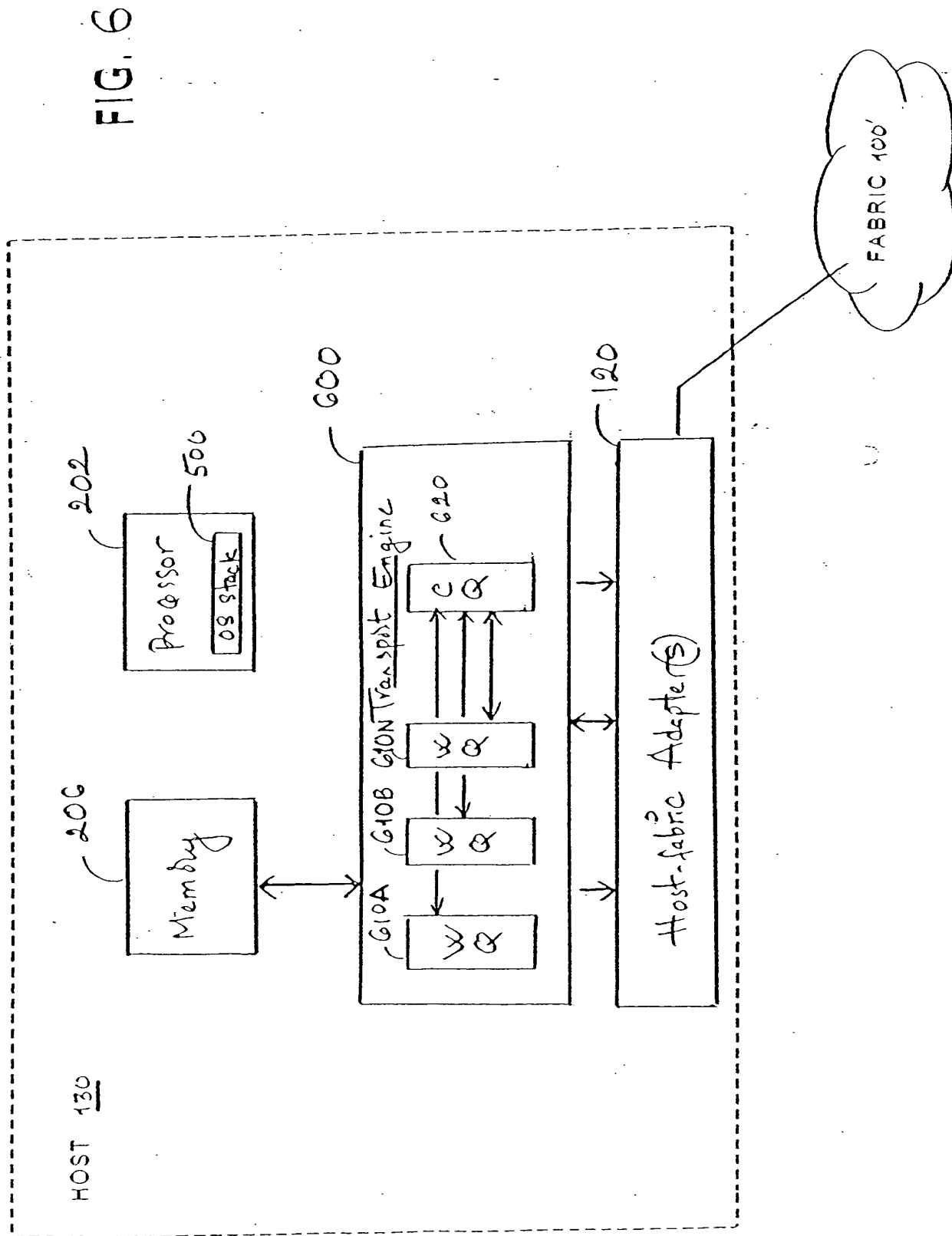
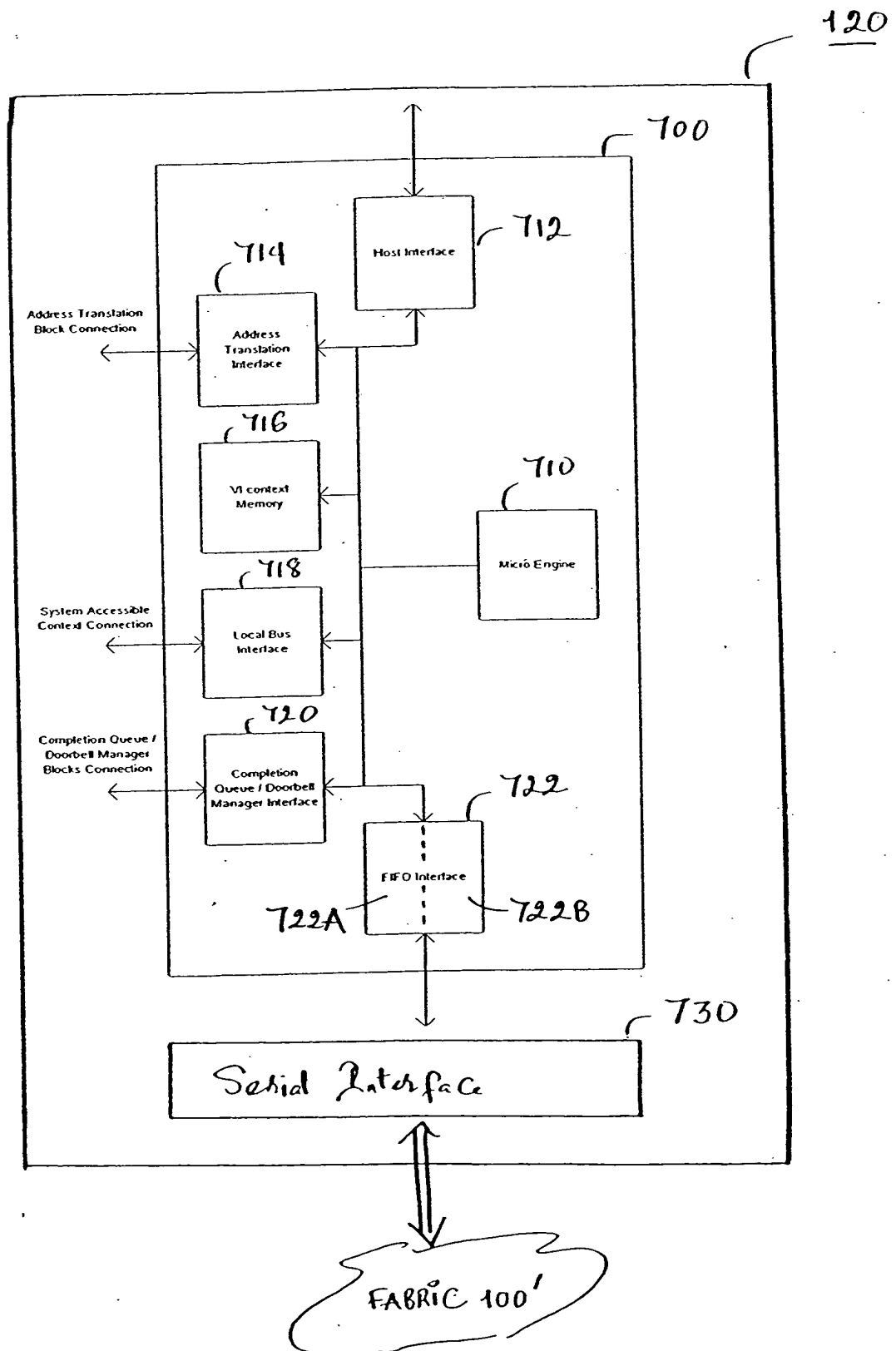


FIG. 7



09734746 120000

FIG. 8

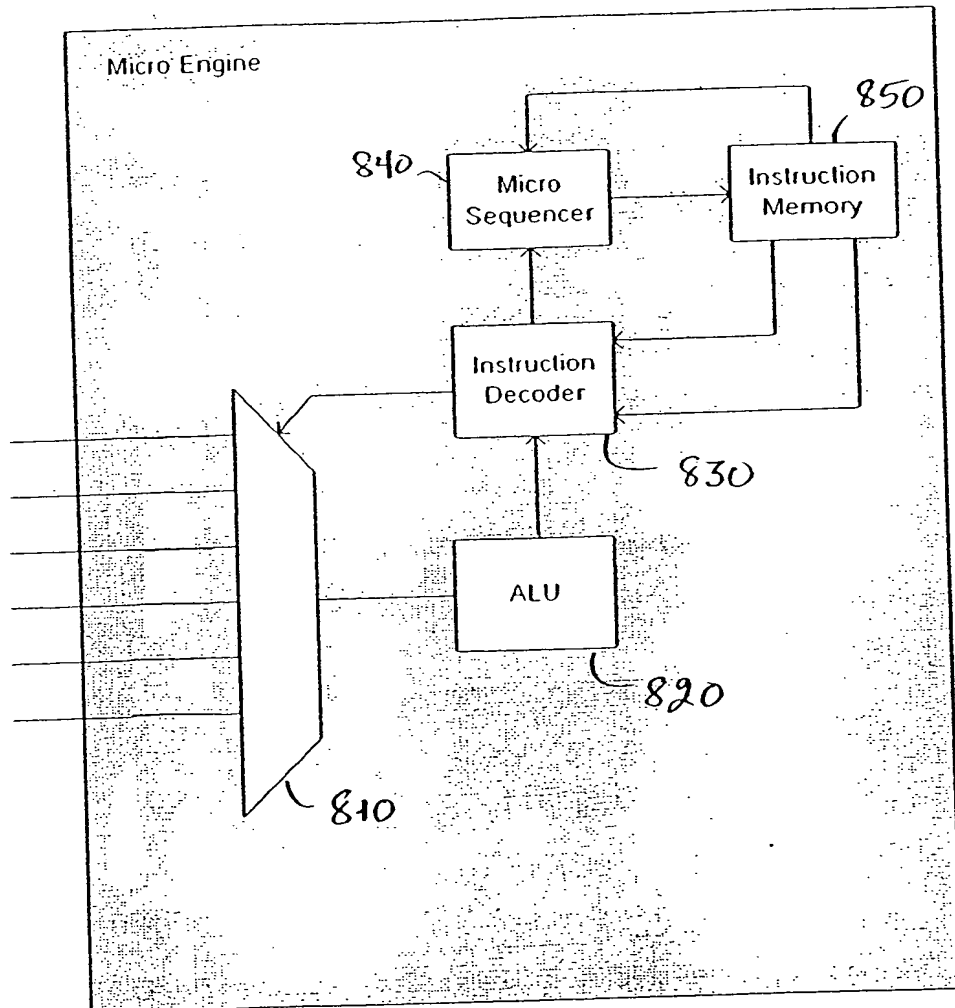
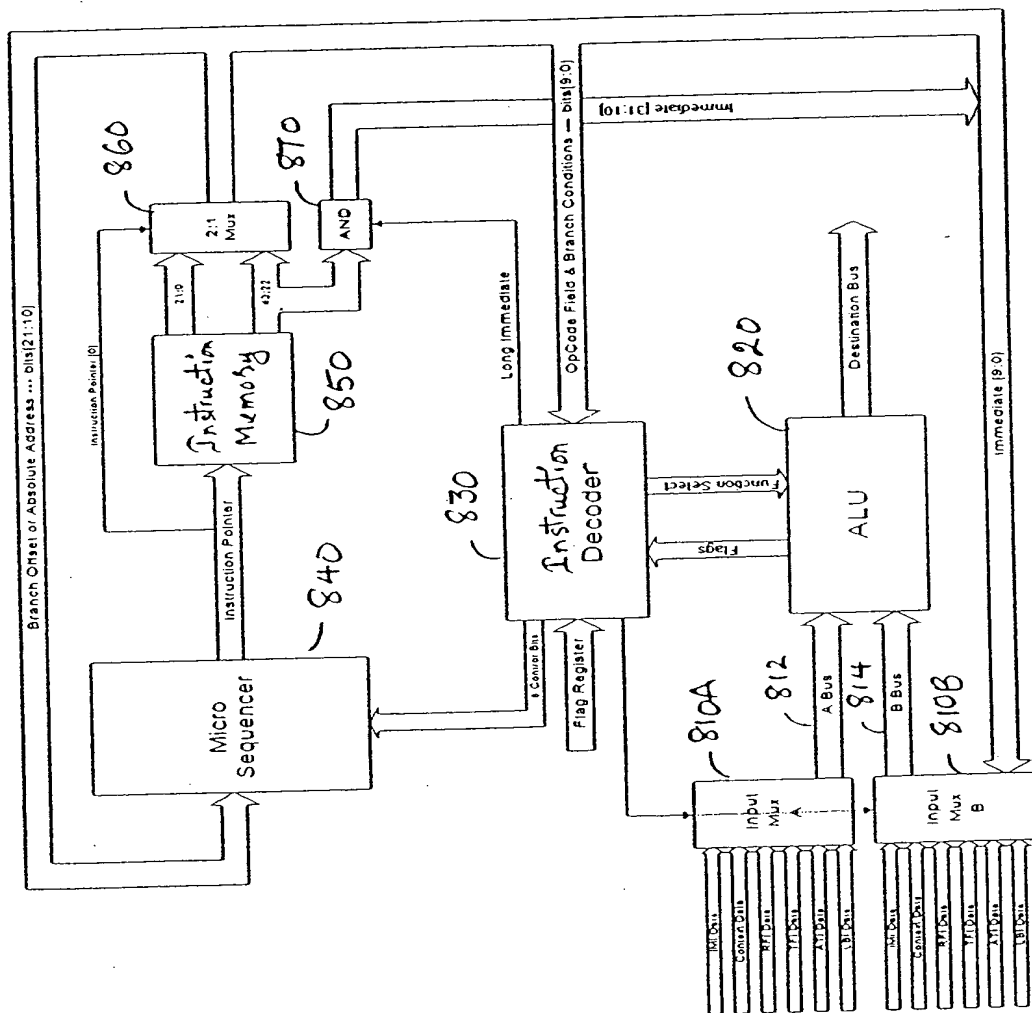
$$\frac{710}{5}$$


FIG. 9



00000001 911200

Single Memory

40x32

MEMORY ADDRESS

39

1110

"A"
15x8
Used

25

24

1120

17

16

1110

10

40 bit
height

FIG. 11A

MEMORY DATA

31

0

"C"
17x32
Used

"B"
8x12
Used

SECRET "SECRET" FIG. 11B

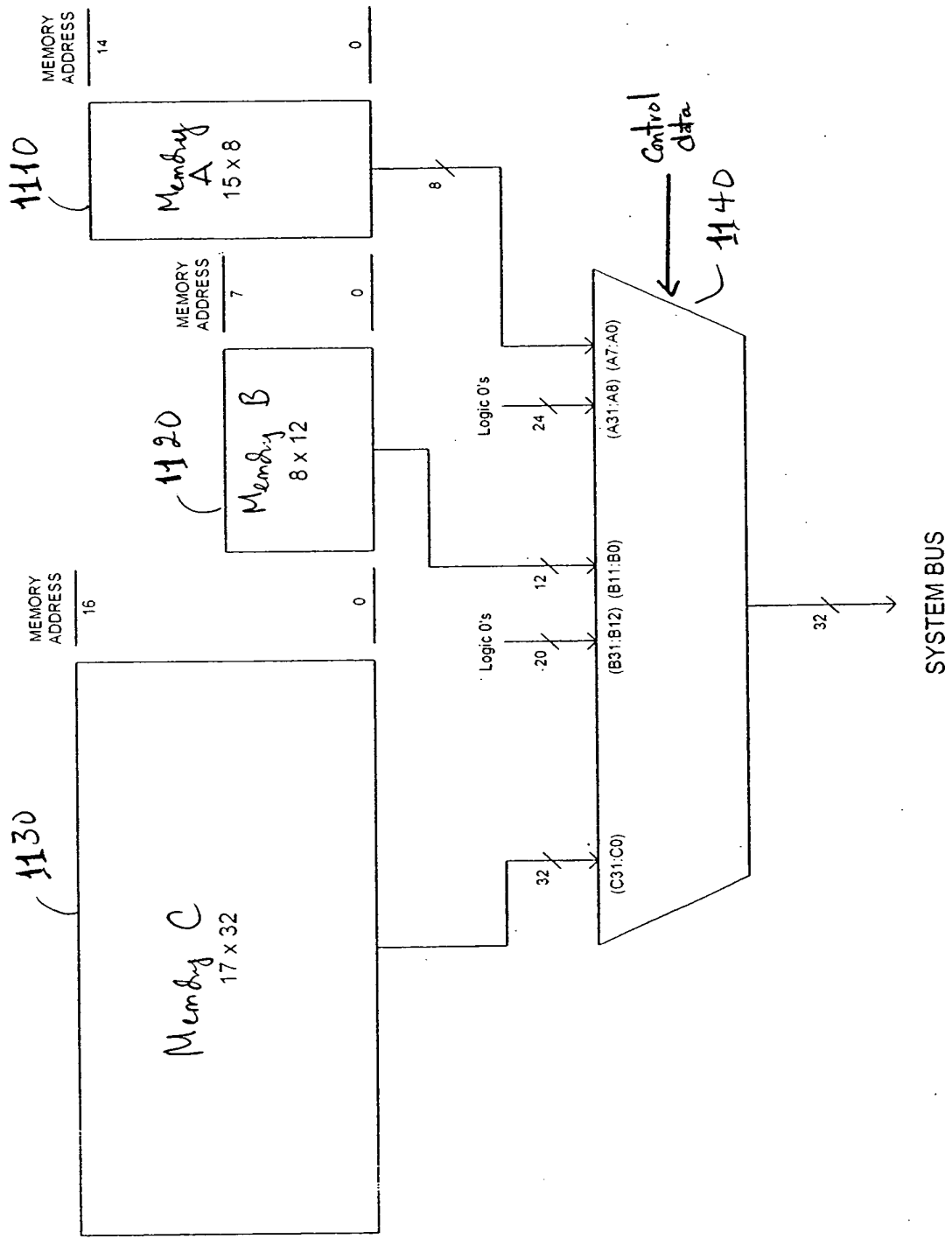
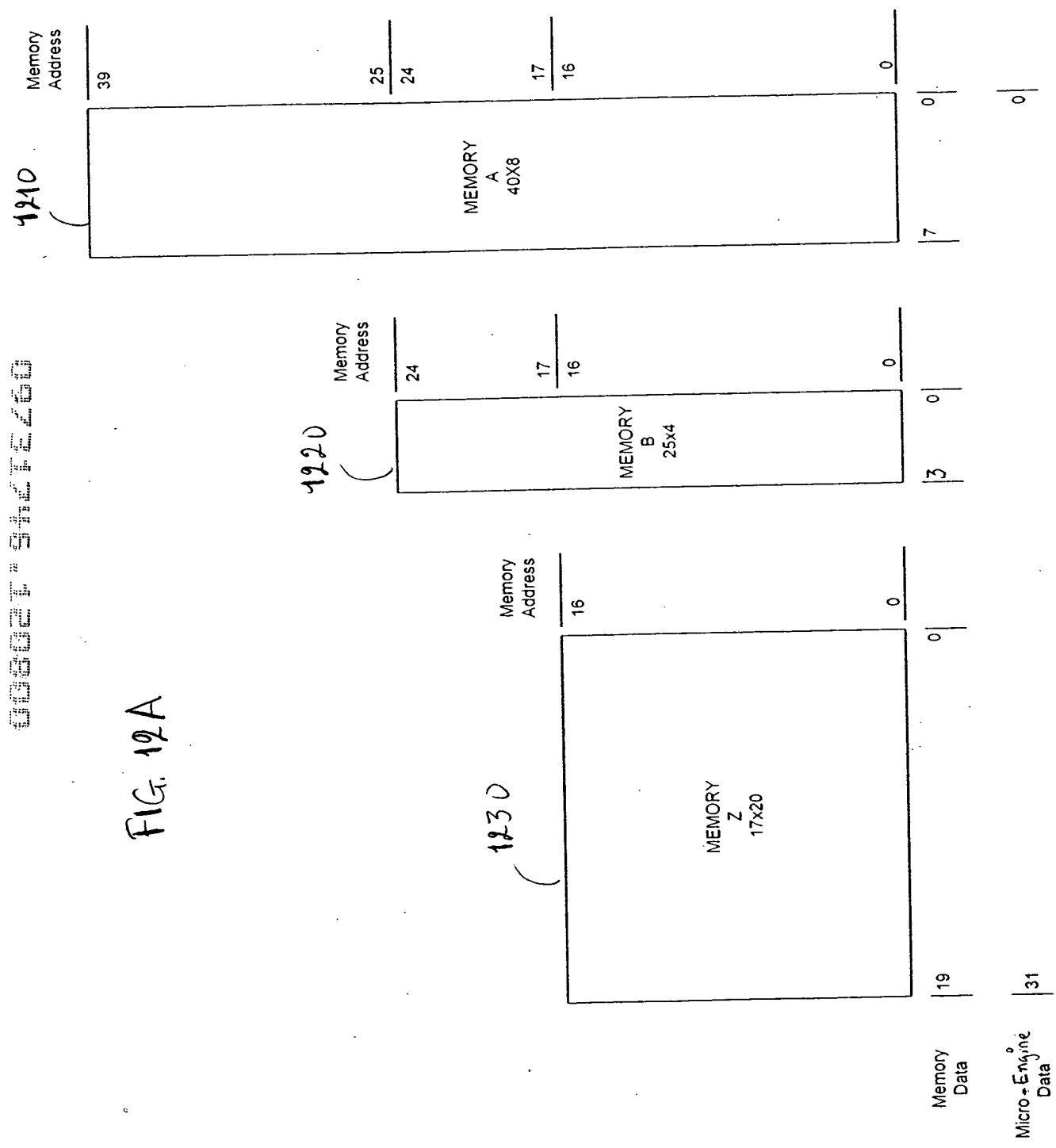


FIG. 12A



000027 9427E66

FIG. 12B

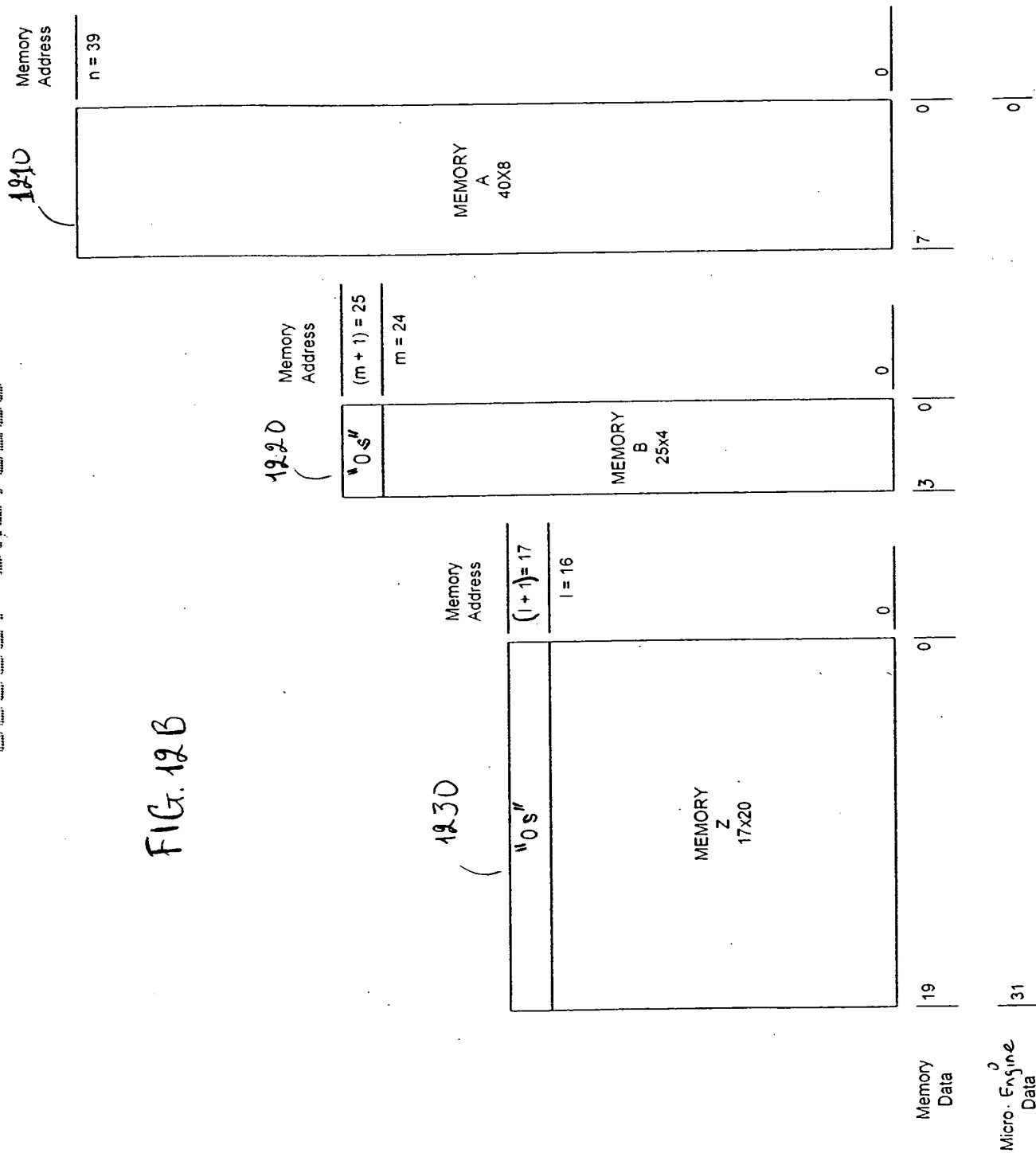


FIG. 13B

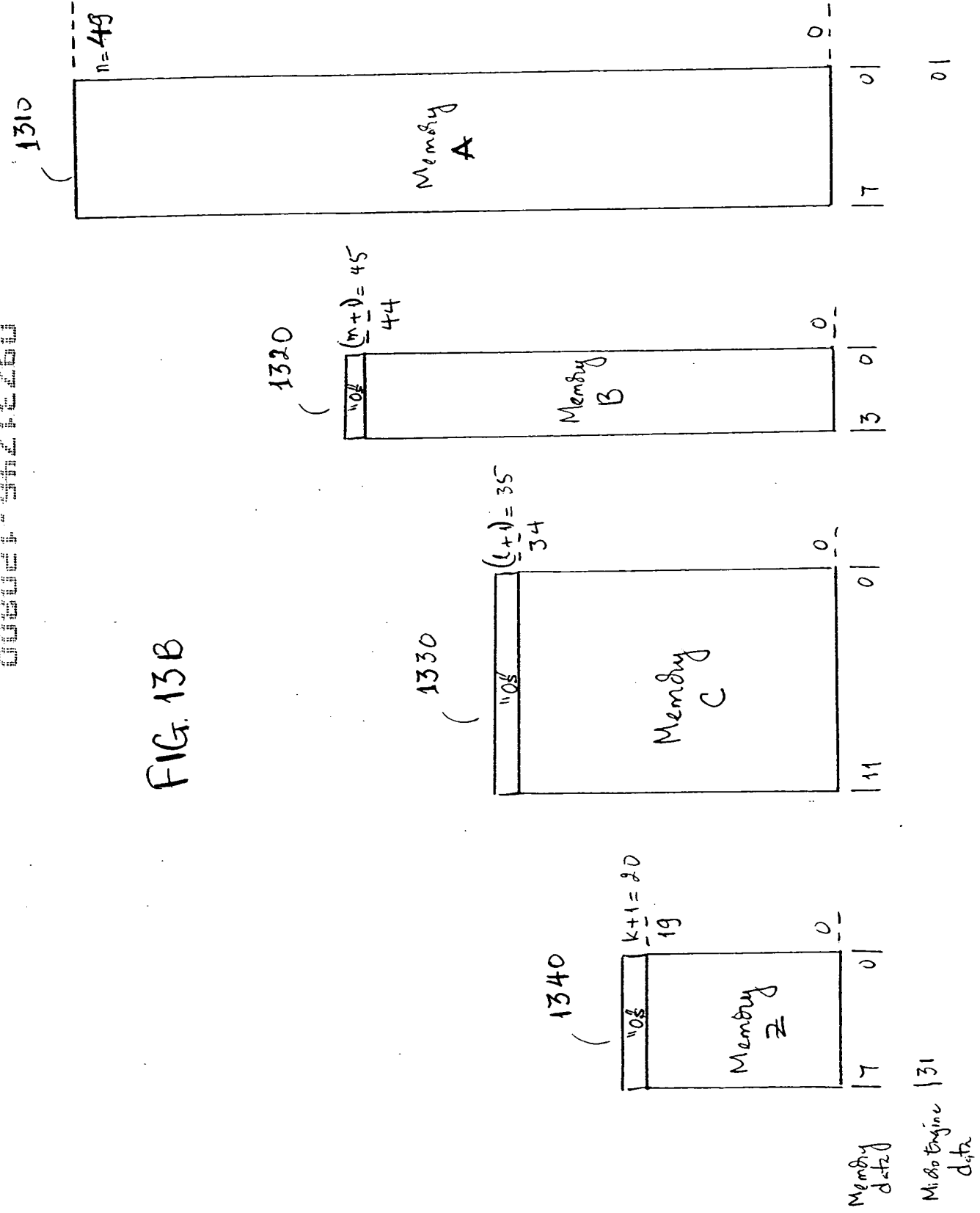


FIG. 14

